



13281 U.S. PTO

NIT-201-03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: N. KATOH et al
Serial No. 10/084,435
Filed: February 28, 2002

Group Art Unit: 2825
Examiner: P. Kik
For: SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE, STORAGE MEDIUM ON WHICH CELL
LIBRARY IS STORED AND DESIGNING METHOD
FOR SEMICONDUCTOR INTEGRATED CIRCUIT

REQUEST FOR CONTINUATION APPLICATION
UNDER 37 C.F.R. 1.53(b)

Commissioner for Patents
Mail Stop Patent Application
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This is a request for filing a continuation application under 37 C.F.R. 1.53(b) of pending prior application Serial No. 10/084,435, filed on February 28, 2002, entitled SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE, STORAGE MEDIUM ON WHICH CELL LIBRARY IS STORED AND DESIGNING METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT, by all of the inventors named in the prior application.

1. Enclosed is a copy of the prior application, including the Declaration as originally filed.

2. The Filing Fee is calculated below:

CLAIMS AS FILED IN THE PRIOR APPLICATION
LESS ANY CLAIMS CANCELED BY AMENDMENT BELOW
PLUS ANY CLAIMS ADDED BY ACCOMPANYING PRELIMINARY AMENDMENT

17497 U.S.PTO
110/827291



3. A Credit Card Payment Form in the amount of \$770.00 is enclosed to cover the Filing Fee.

4. The Commissioner is hereby authorized to charge any additional fees which may be required, or to credit any overpayment, to Deposit Account No. 50-1417.

5. New drawings are enclosed, eighteen (18) sheets, Figs. 1-28.

6. The power of attorney is set forth in the Declaration in the prior application or an associate power of attorney is hereby granted to:

Jeffrey M. Ketchum, Registration No. 31,174
Shrinath Malur, Registration No. 34,663
John R. Mattingly, Registration No. 30,293
Daniel J. Stanger, Registration No. 32,846
Gene W. Stockman, Registration No. 21,021.

There is no change in the correspondence address.

Address all future correspondence to:

MATTINGLY, STANGER & MALUR, P.C.
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314
(703) 684-1120.

Please associate this application with Customer No. 24956.

9. The prior application is assigned to Renesas Technology Corp.

10. Priority of the following Japanese patent application is claimed under 35 U.S.C. § 119:

No. 9-359277, filed December 26, 1997.

The certified priority document has been filed in the PCT application.

The undersigned hereby declares that no matter contained in the specification, including the claims, and drawings filed in the present continuation application would have been new matter in the prior application Serial No. 10/084,435, as originally filed on February 28, 2001.

Shrinath Malur R N 21,021
Shrinath Malur
Registration No. 34,663
Attorney of Record

MATTINGLY, STANGER & MALUR
1800 Diagonal Road, Suite 370
Alexandria, Virginia 22314
(703) 684-1120
Date: April 20, 2004